CMC704-SERIES

RoHS Compliant

- **Ultra low phase jitter: 0.5ps (12kHz to 20MHz)**
- **LVCMOS/ LVTTL compatible output**
- **SMD package 7.0 x 5.0 mm**

ELECTRICAL SPECIFICATIONS

PARAMETER	SYMBOL	CONDITION	VALUE			UNIT
			Min.	Typ.	Max.	
Frequency Range	f_0	Any Frequency between Frequency range, accurate to 6 decimal places	80		220	MHz
Supply Voltage	Vs	Supply voltages between 2.5V and 3.3V can be supported in increments of 0.1V	1.71 2.25 2.52 2.97	1.8 2.5 2.8 3.3	1.89 2.75 3.08 3.63	V V V V
Supply Current	Is	Vs = 1.8V, f_0=100MHz, no load Vs = 2.5V,2.8V and 3.3V f_0=100MHz, no load		30 34	33 36	mA mA
Operating Temperature	T_a	Extended Commercial Industrial	-20 -40		+70 +85	°C °C
Frequency Stability	$\Delta f/f_0$	Including First Year aging, initial frequency tolerance at 25°C, Frequency stability over temperature range, supply variation, load variation	-10 -20 -25 -50		+10 +20 +25 +50	ppm ppm ppm ppm
Long term stability, aging	$\Delta f/\Delta t_y$ $\Delta f/\Delta t_y$	First year 10 years	-1.5 -5.0		1.5 5.0	ppm ppm
Enable / Disable Time	$T_{E/D}$	f_0=220MHz, for other frequencies, $T_{E/D}$ =100ns+ 3 cycles			115	ms
Enable / Disable Current	$I_{E/D}$	Vs=1.8V, E/D =GND, output is weakly pulled down Vs=2.5V,2.8Vor 3.3V, E/D =GND output is weakly pulled down			30 31	mA mA
Standby Current	Istby	STBY=GND,Vs=1.8V STBY=GND,Vs=2.5V,2.8V or 3.3V Output is weakly pulled down			10 70	µA µA
Startup Time	T_{ST}	Measured from the time Vs reaches its rated maximum value		6	10	ms
Resume Time	T res	Measured from the time STBY pin crosses 50% threshold			10	ms
Rise/ Fall Time	Tr / Tf	C_L = 15pF, 10% - 90% Vs		1.5	2.0	ns
RMS Phase Jitter	J_{PH}	f_0=156.25MHz,BW 12KHz to 20MHz		0.6	1.0	ps
RMS Period Jitter	J_P	f_0=156.25MHz,Vs=2.5V,2.8V or 3.3V f_0=156.25MHz,Vs=1.8V		1.5 2.0	2.0 3.0	ps ps
Input Voltage High	VIH	Pin 1, E/D or STBY	70%			Vs
Input Voltage Low	VIL	Pin 1, E/D or STBY			30%	Vs
Input pull-up impedance	Z in			100	250	kΩ

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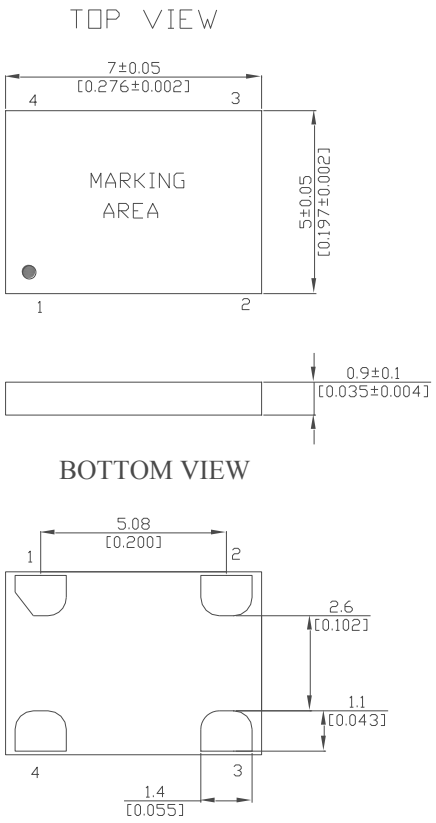
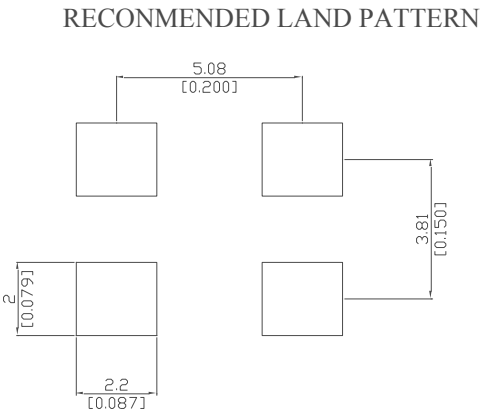
MECHANICAL DIMENSIONS AND PIN FUNCTIONING





PIN	SYMBOL	FUNCTION
1	E/D/STBY	E/D H or Open* :Enable output frequency L :Disable output frequency , high impedance STBY H or Open* : Enable output frequency L : Output is low (weak pull down) Device goes to sleep mode. Supply current (Is) reduces to Istby
2	GND	Electrical Ground
3	OUTPUT	Output Signal
4	Vs	Supply Voltage